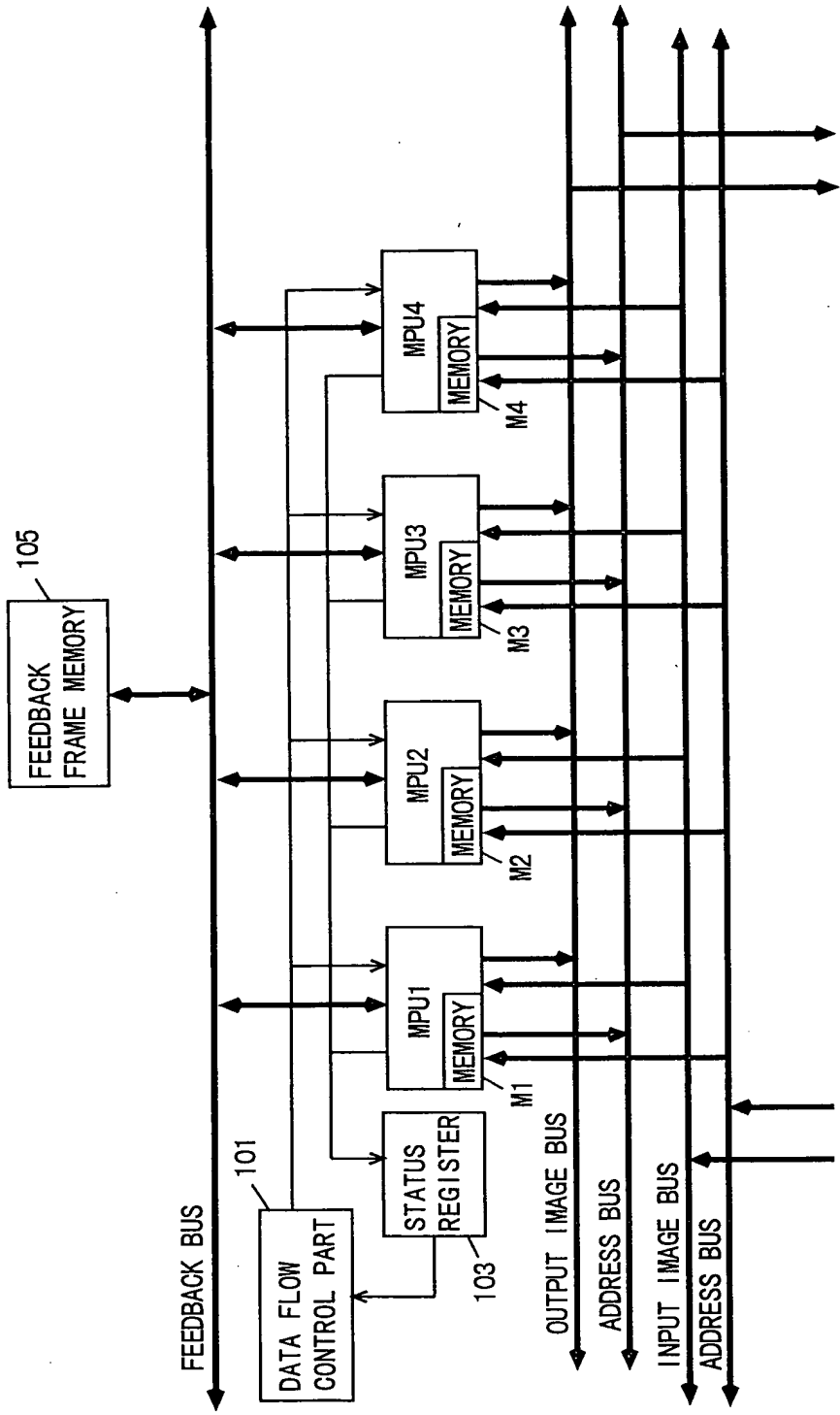


FIG. 1



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FIG. 2

EXEMPLARY STATUS REGISTER

	STANDBY	INPUT	PROCESSING	END OF PROCESSING	OUTPUT
MPU1	0	1	0	0	0
MPU2	1	0	0	0	0
MPU3	0	0	1	0	0
MPU4	0	0	0	0	1

FIG. 3

FLOW OF PROCESSING OF  
DATA FLOW CONTROL PART

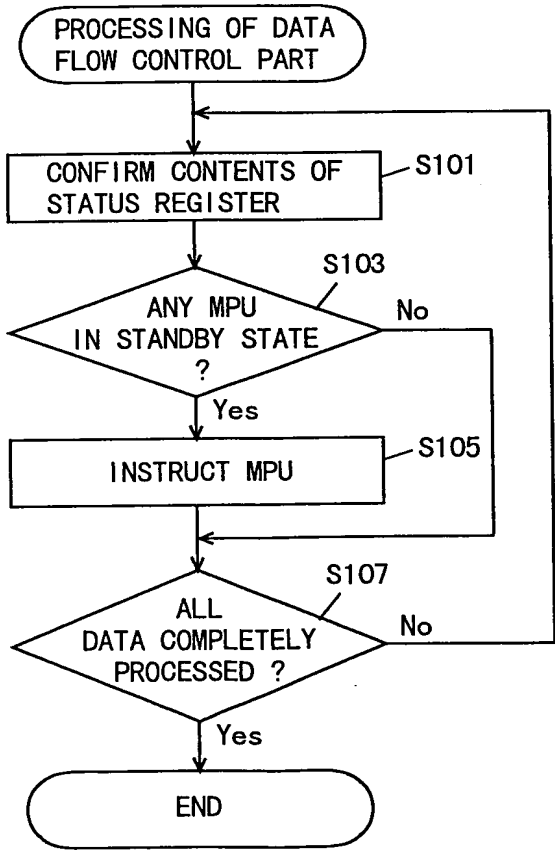
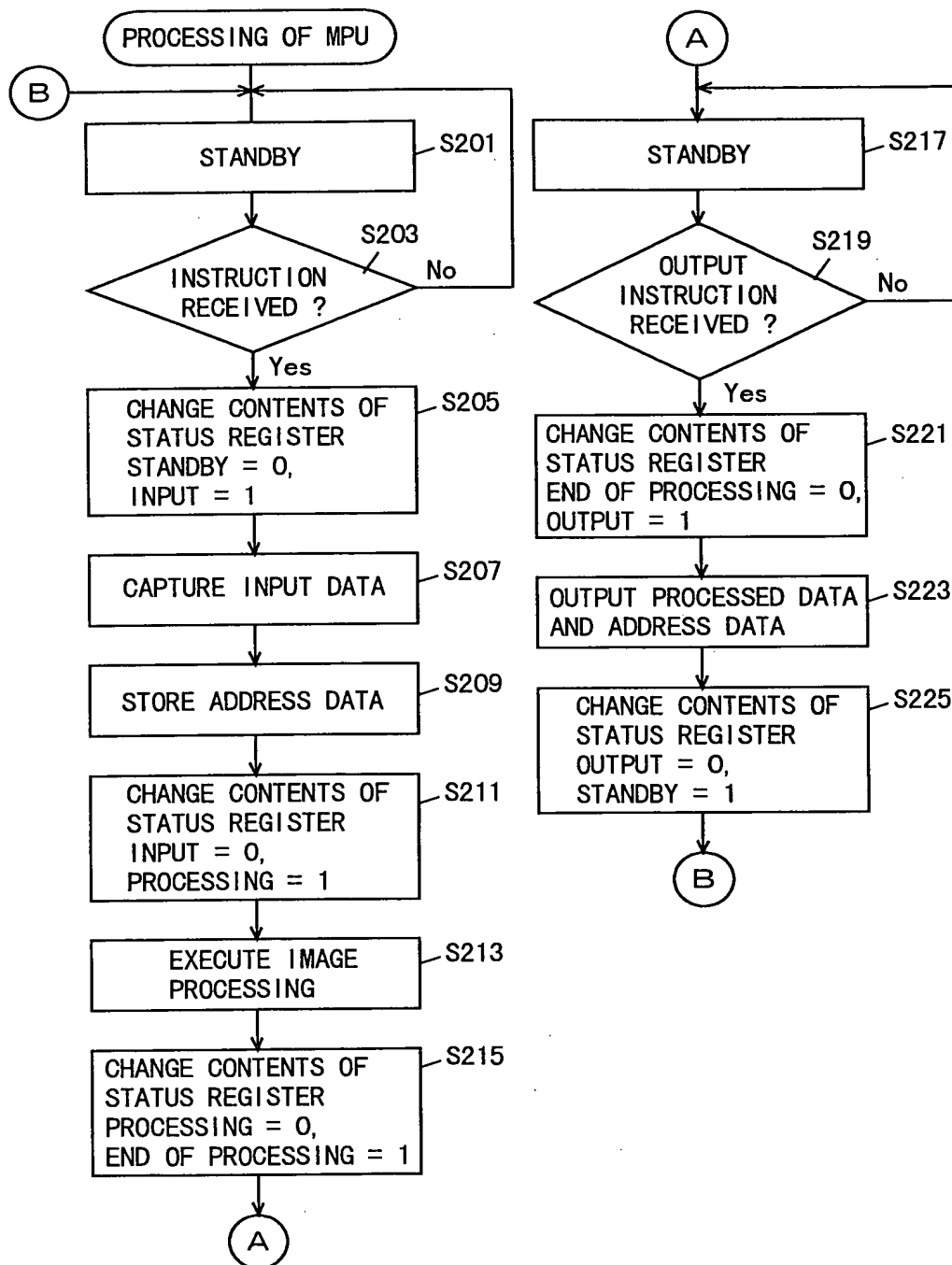


FIG. 4

FLOW OF PROCESSING OF MPU

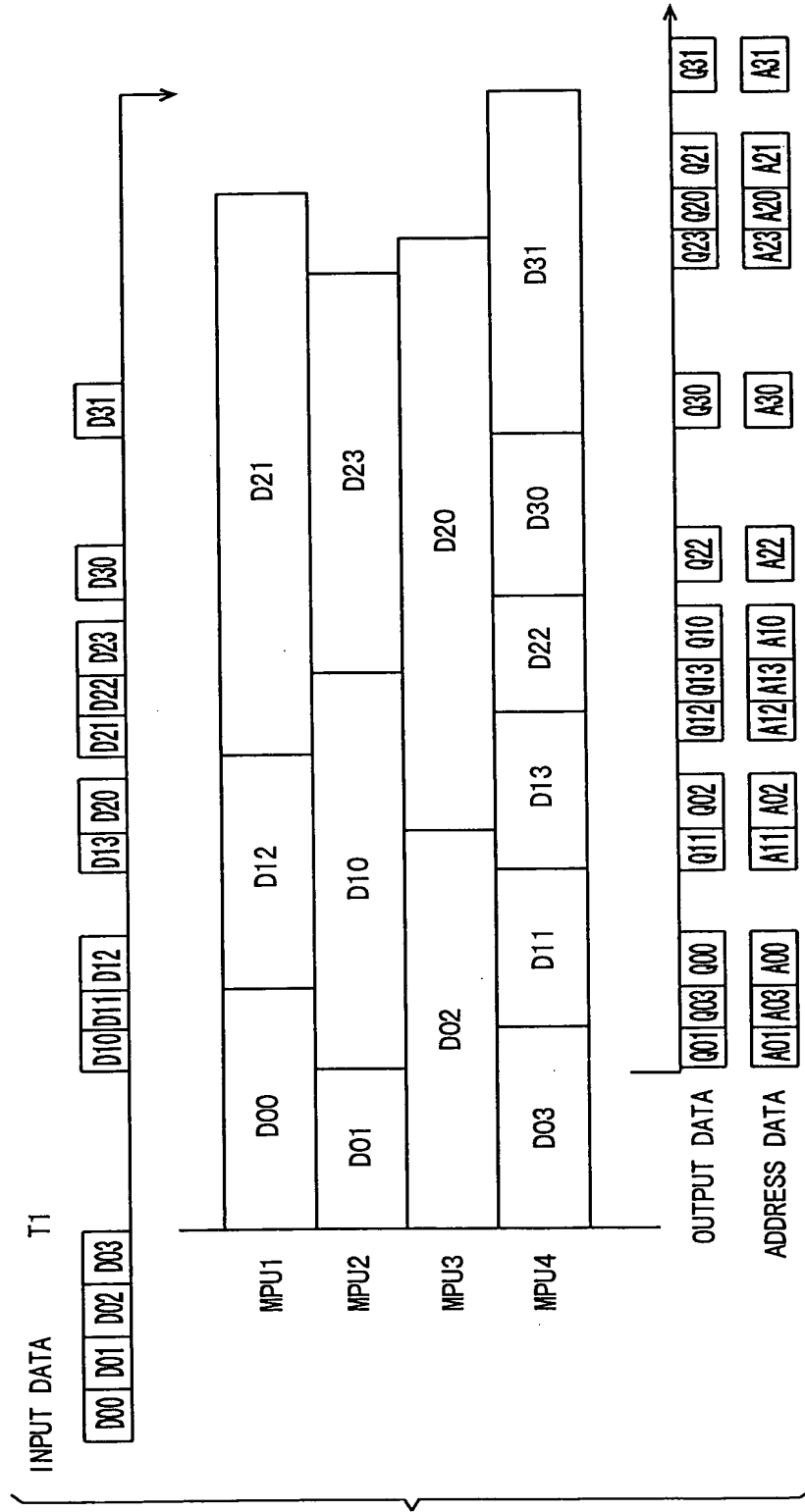


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FIG. 5



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FIG. 6

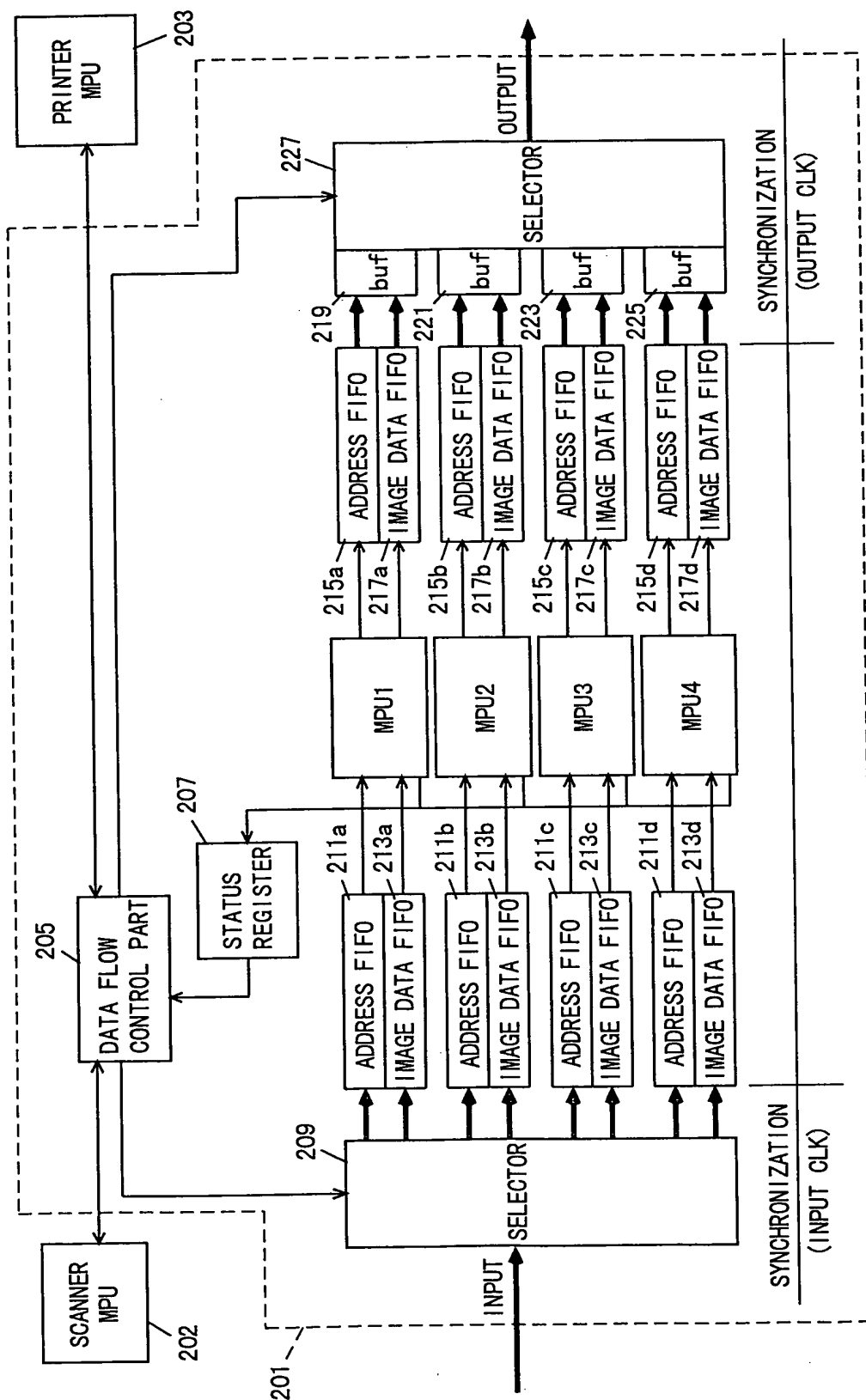
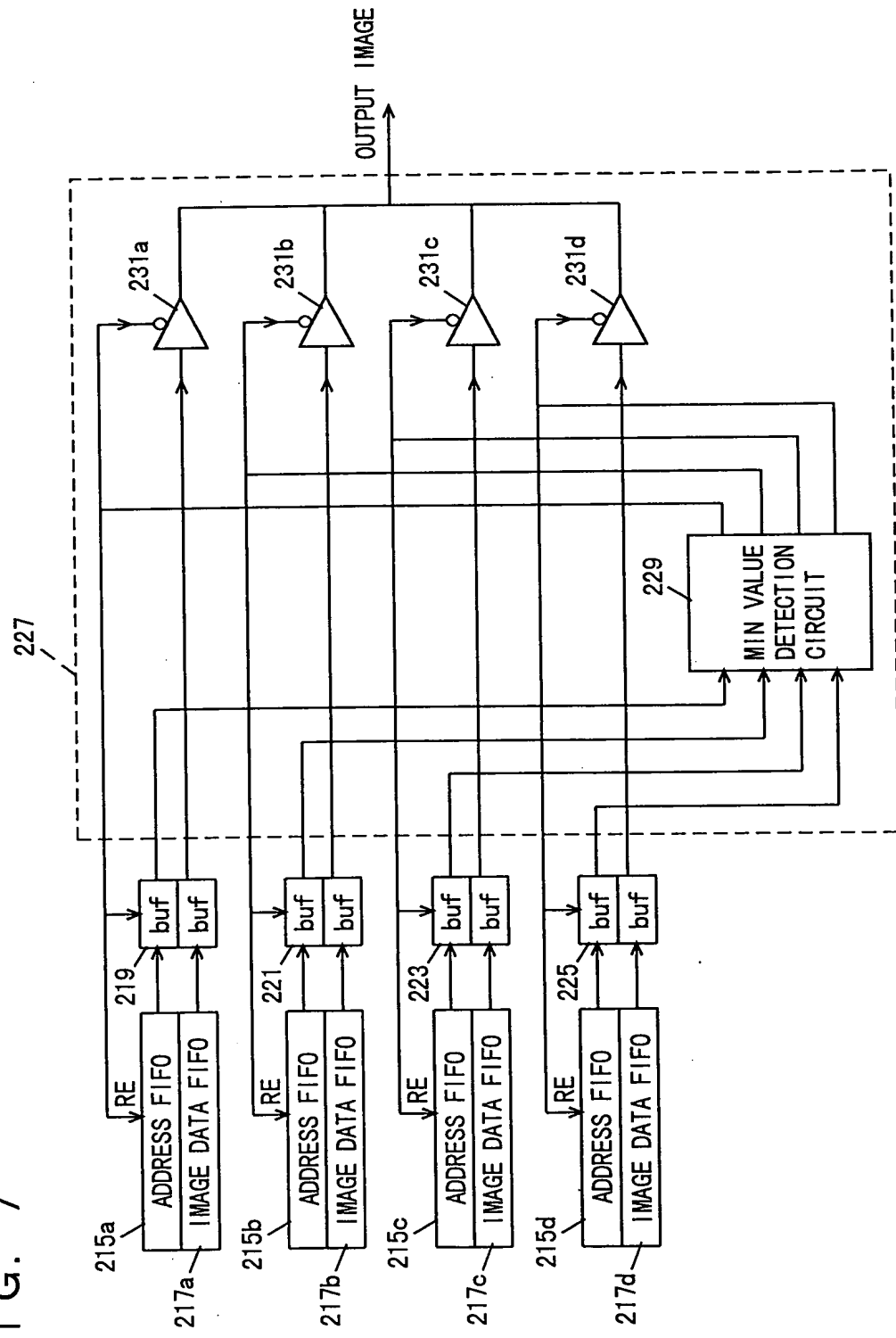


FIG. 7

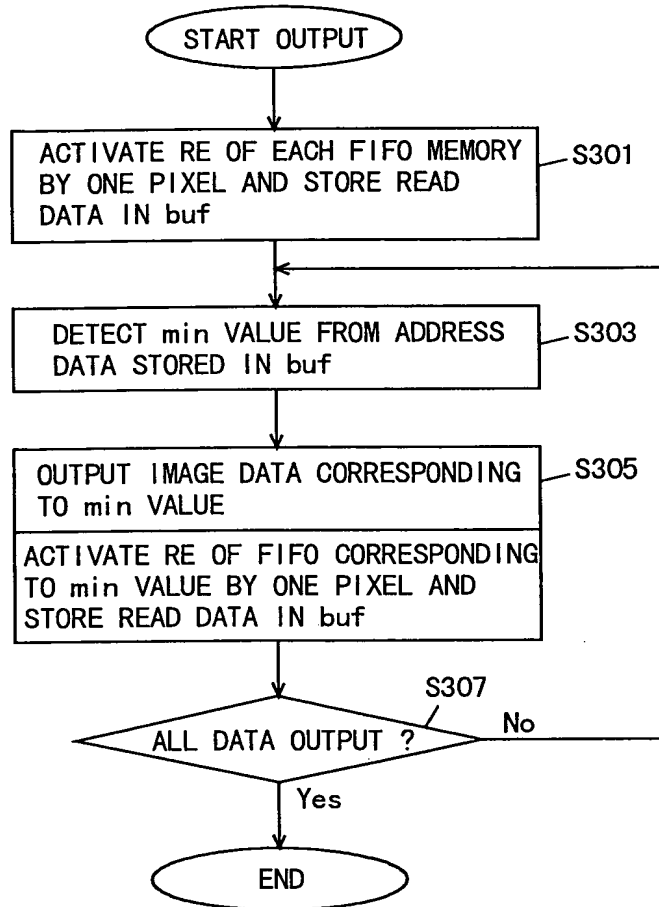


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FIG. 8

OPERATION OF SELECTOR 227



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FIG. 9

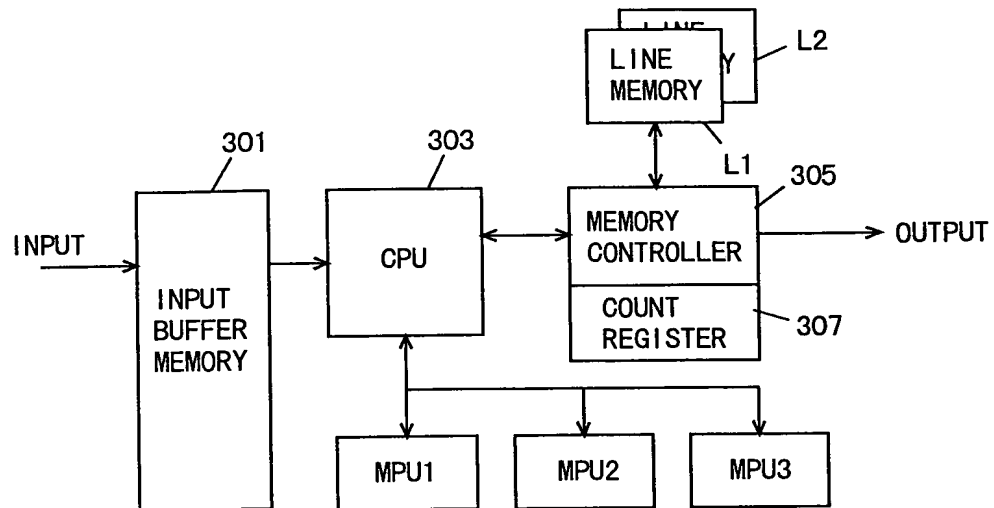


FIG. 10

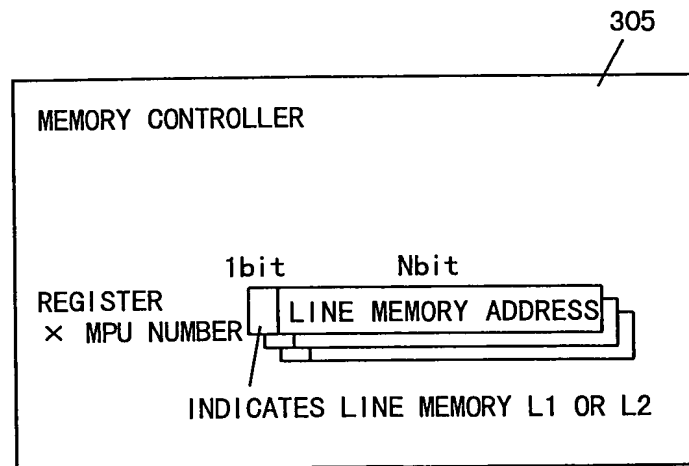




FIG. 11

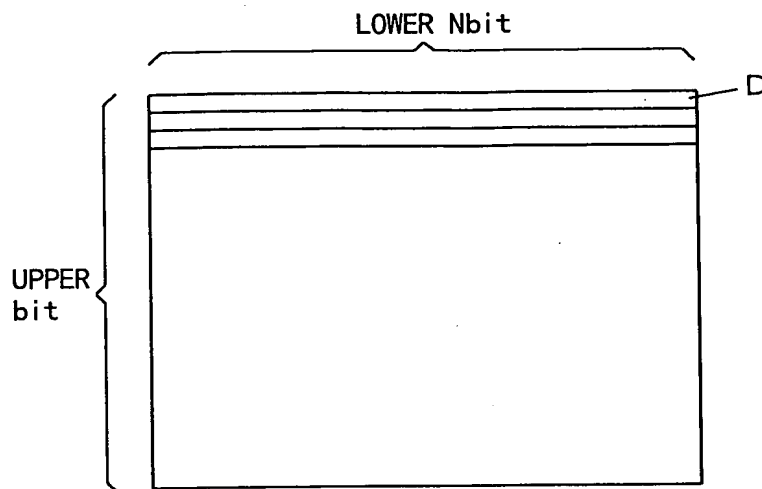
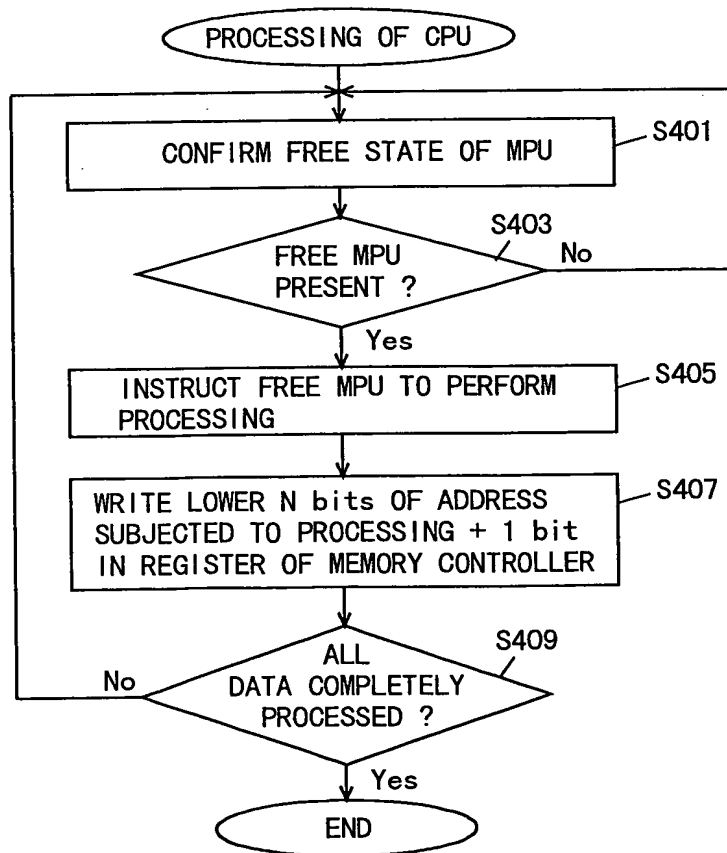


FIG. 12



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FIG. 15

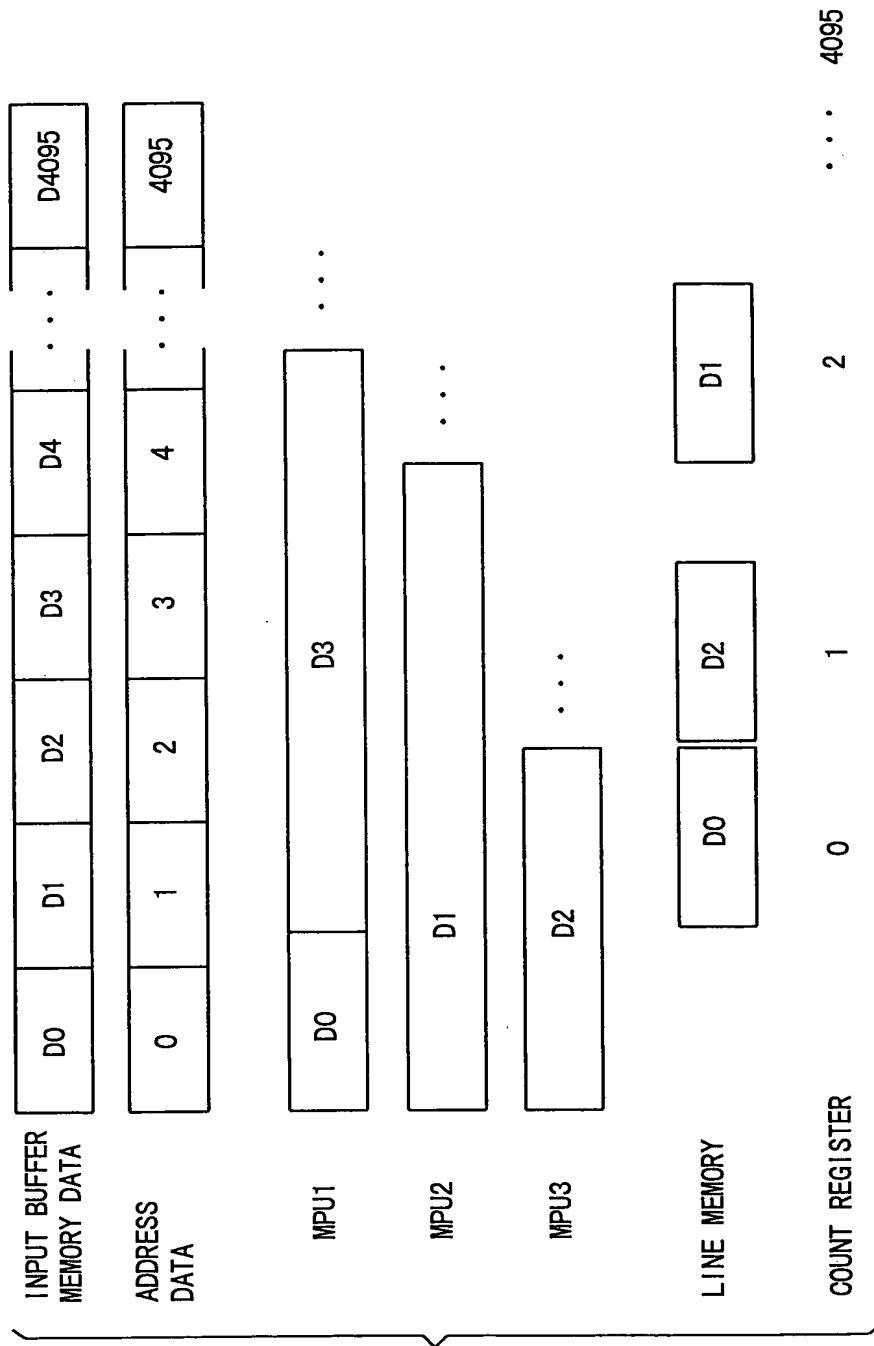
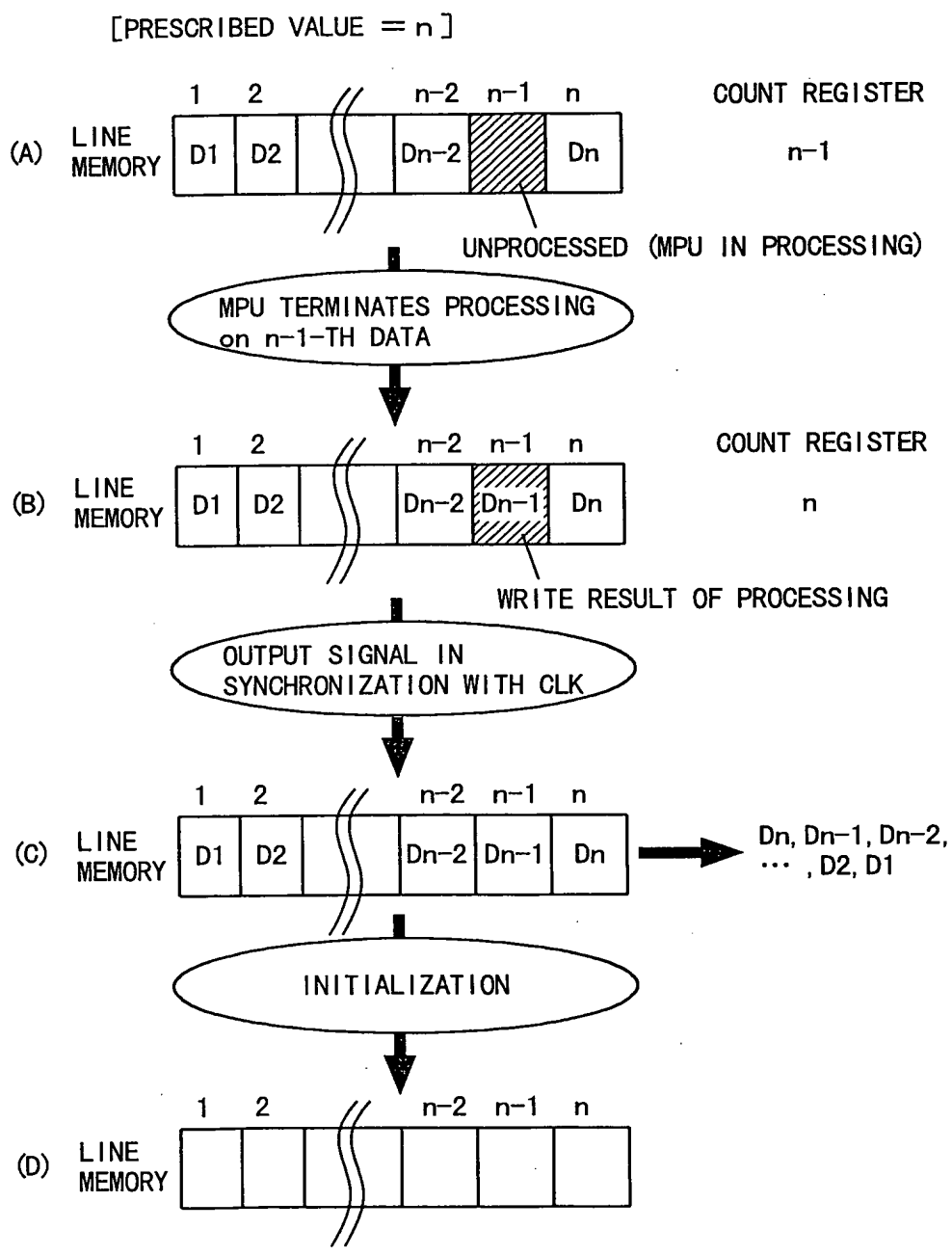


FIG. 16



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FIG. 17

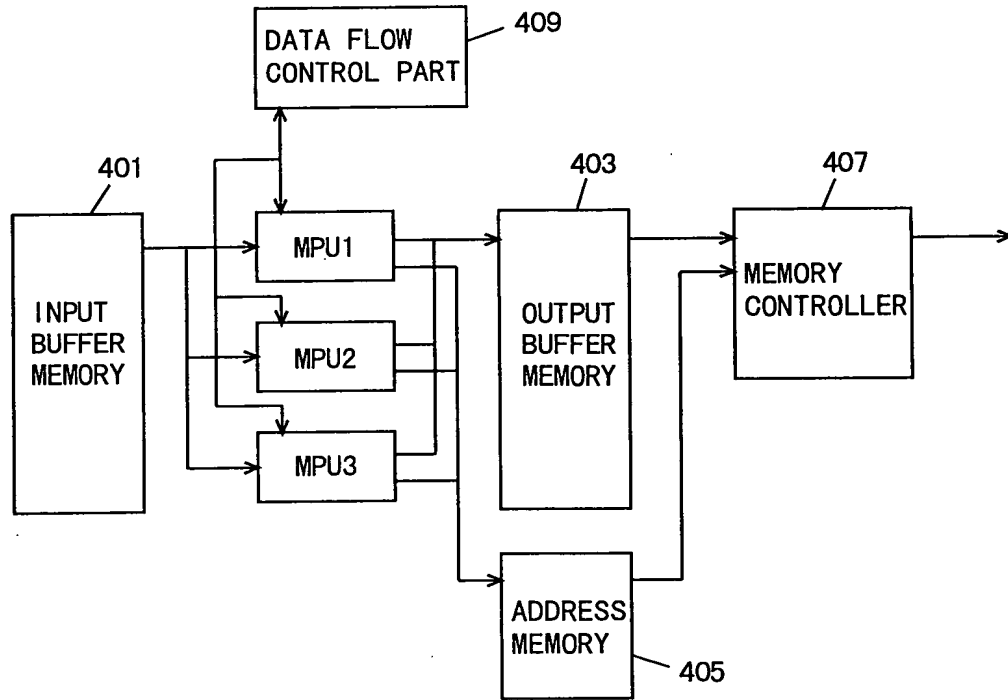
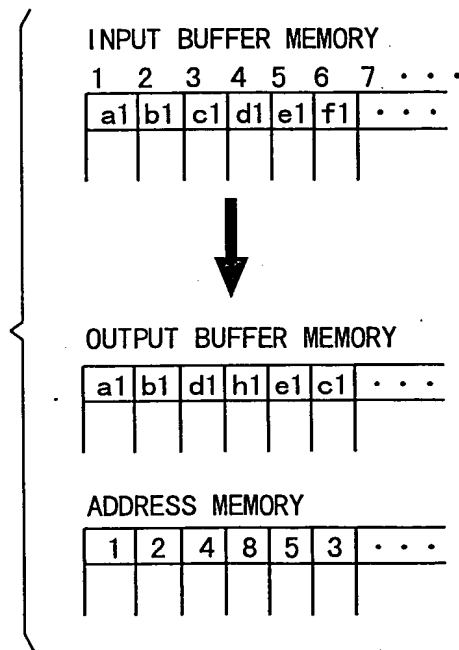


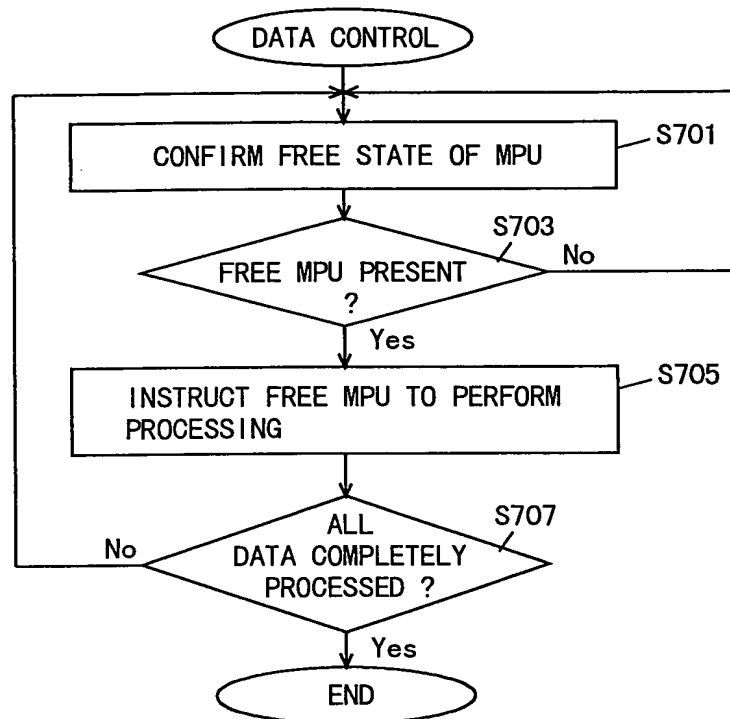
FIG. 18



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FIG. 19

[DATA FLOW CONTROL PART]



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F I G. 21

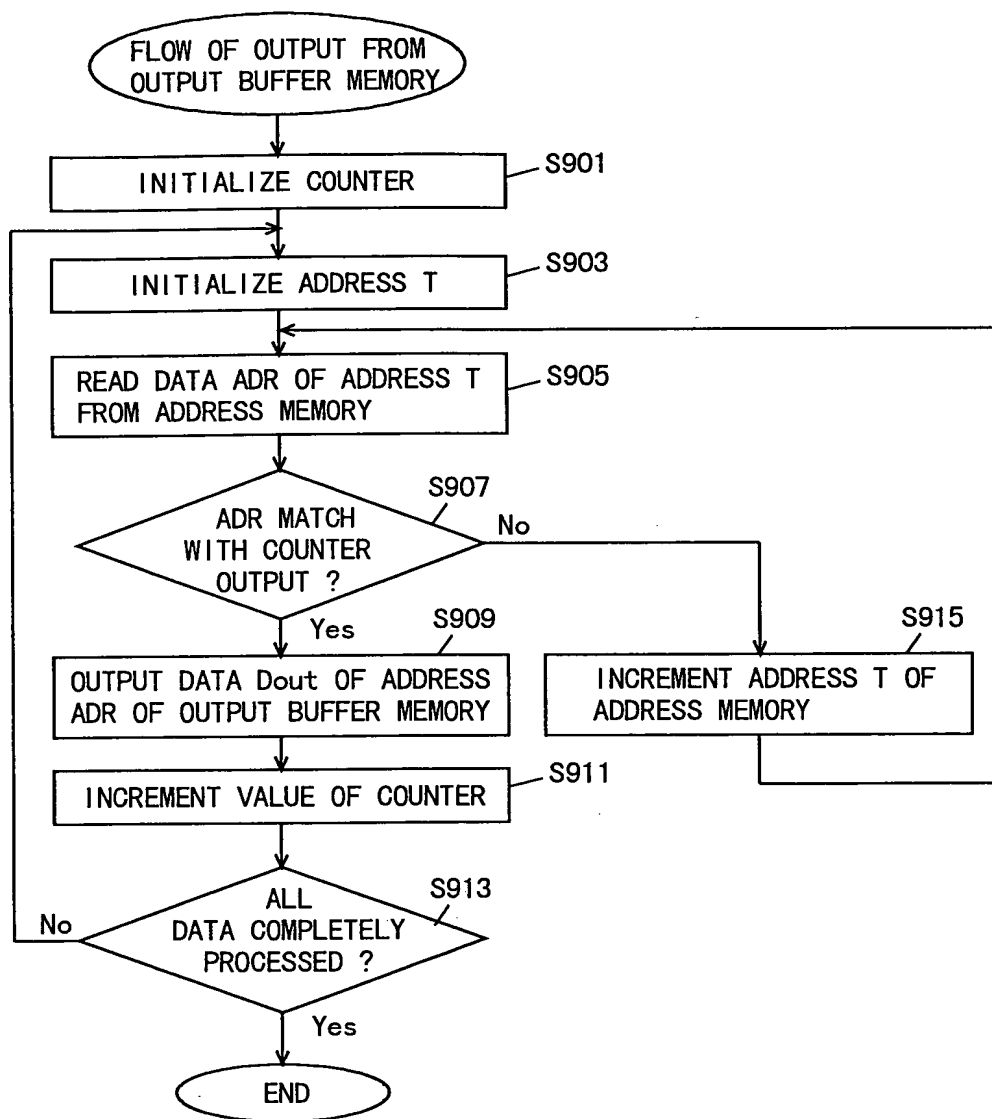


FIG. 22

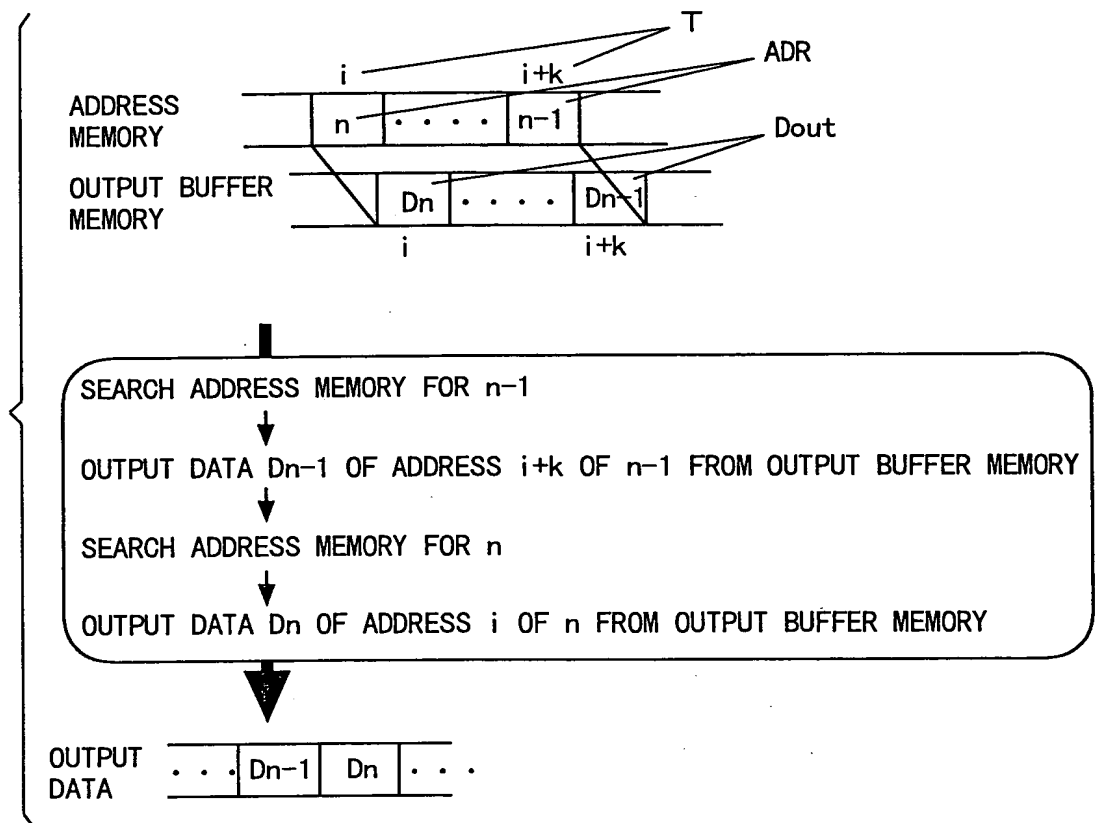


FIG. 25

L1	L2	L3	L4
D00	D01	D02	D03
D10	D11	D12	D13
D20	D21	D22	D23
D30	D31	D32	D33
D40	D41	D42	D43
D50	D51	D52	D53

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FIG. 23A

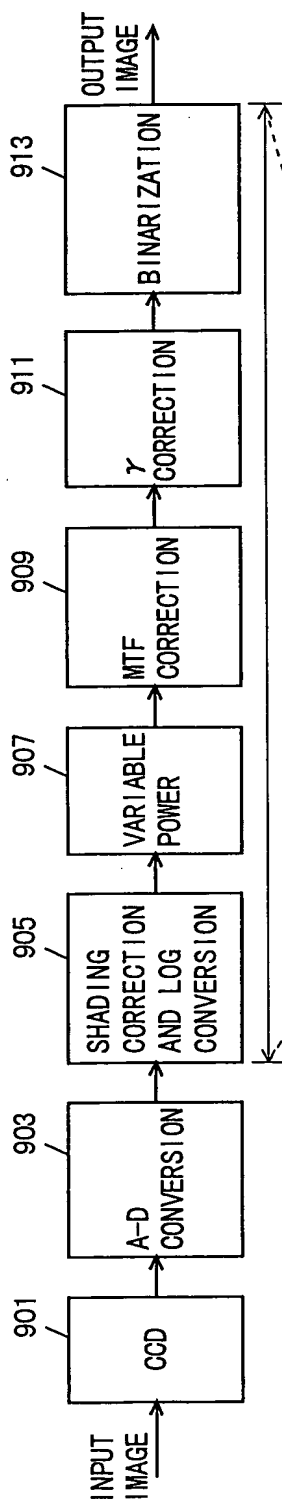


FIG. 23B

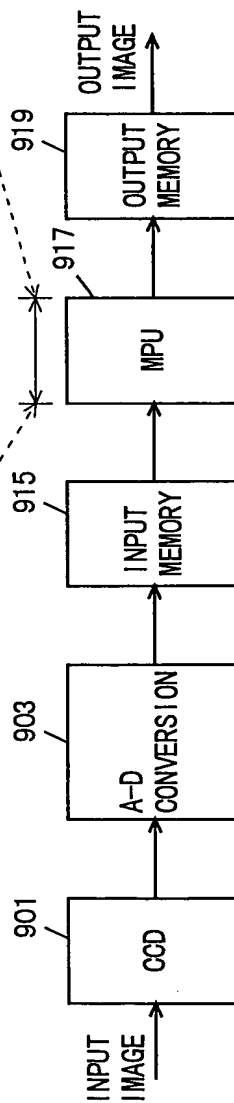
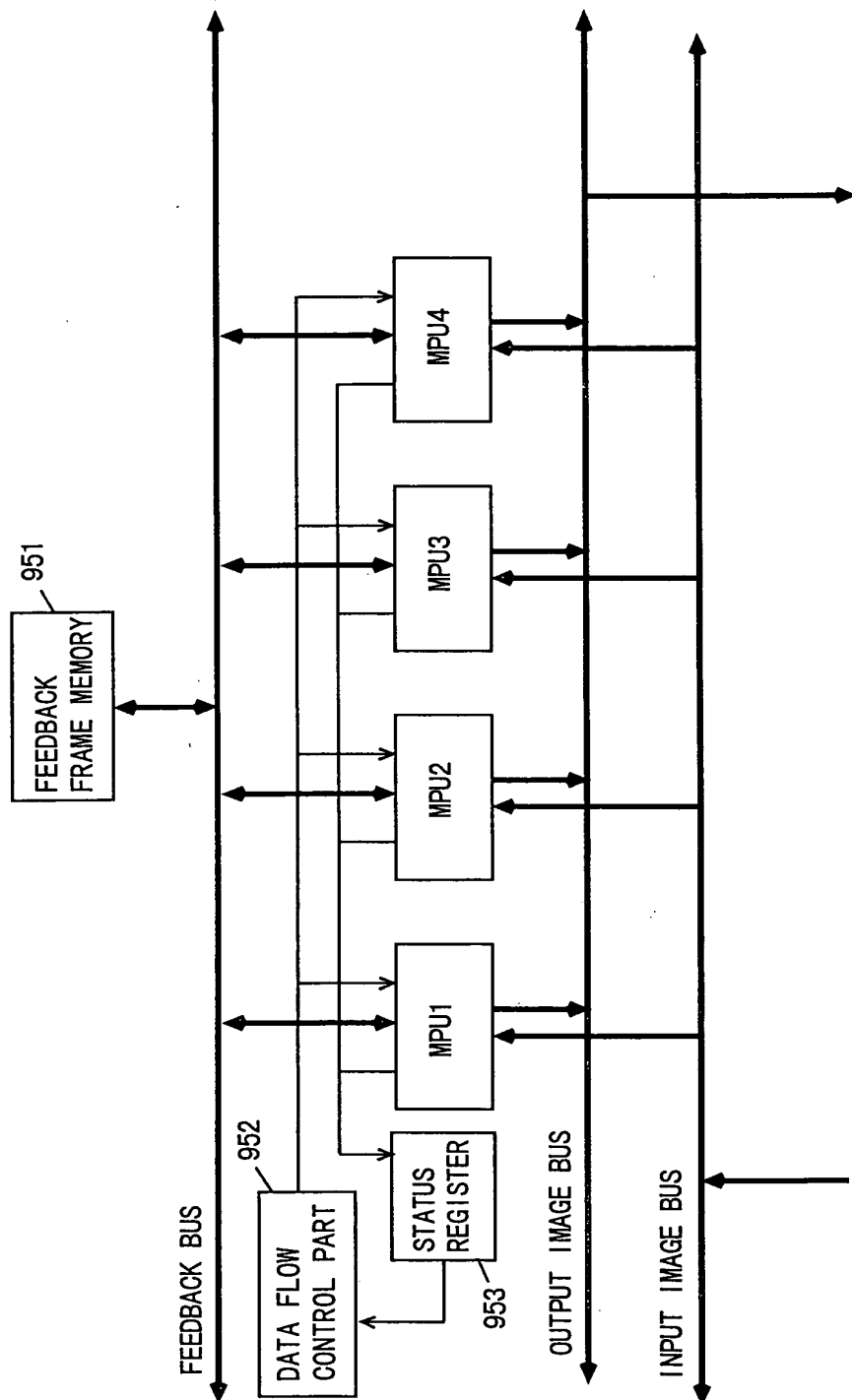


FIG. 24



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FIG. 26

